

Fig. 1a

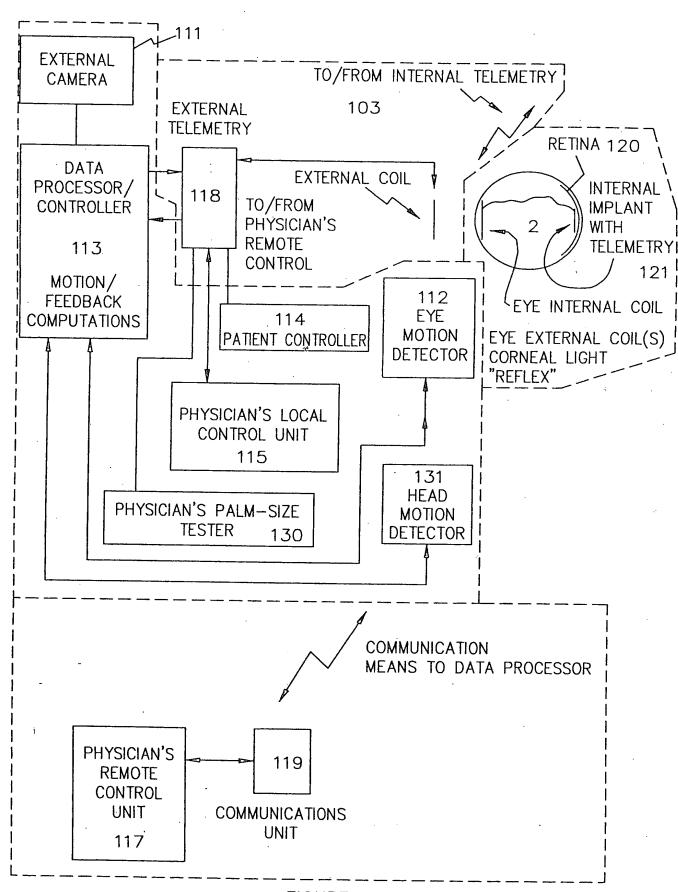


FIGURE 1b

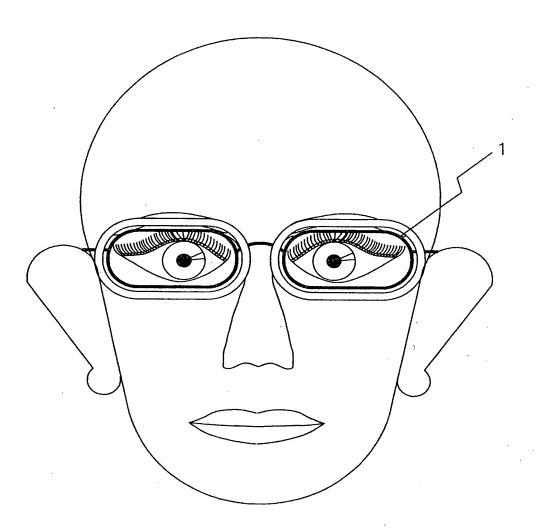
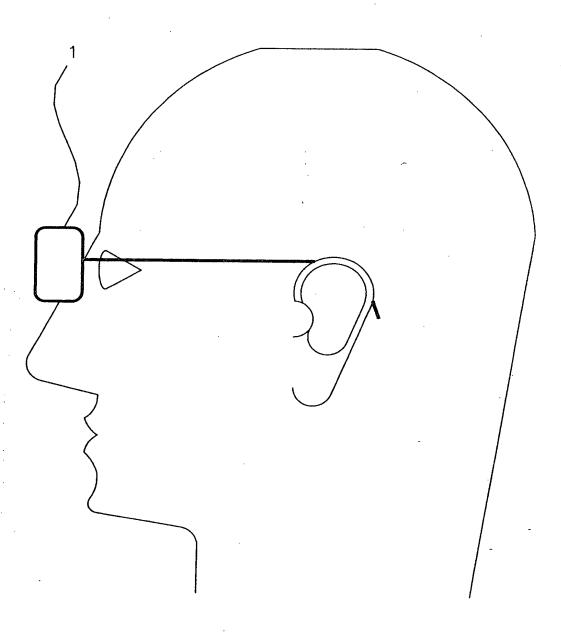
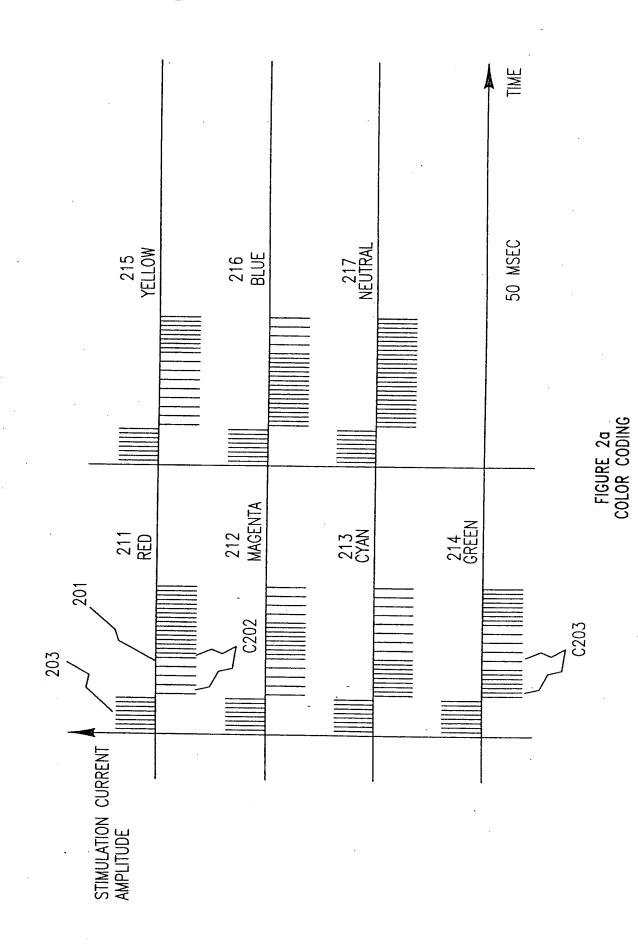
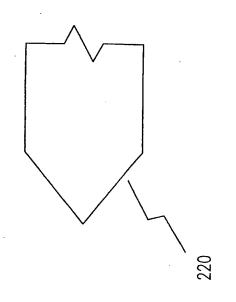


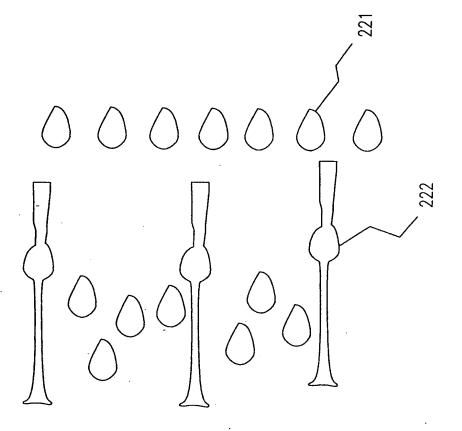
Fig. 1c



. Fig 1d







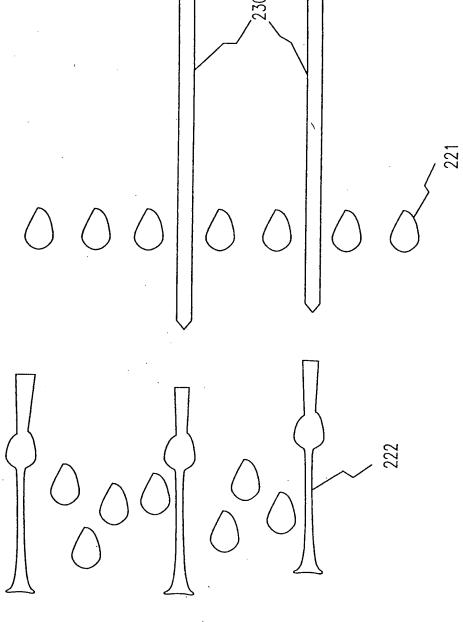
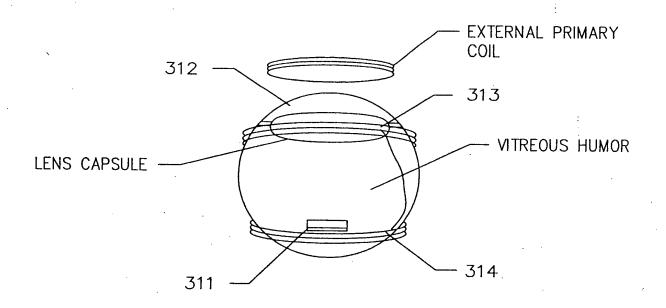


FIGURE 2c



EXTERNAL/INTERNAL TELEMETRY UNIT — EXTERNAL COIL/INTERNAL COIL /INTERNAL CHIP

FIGURE 3a

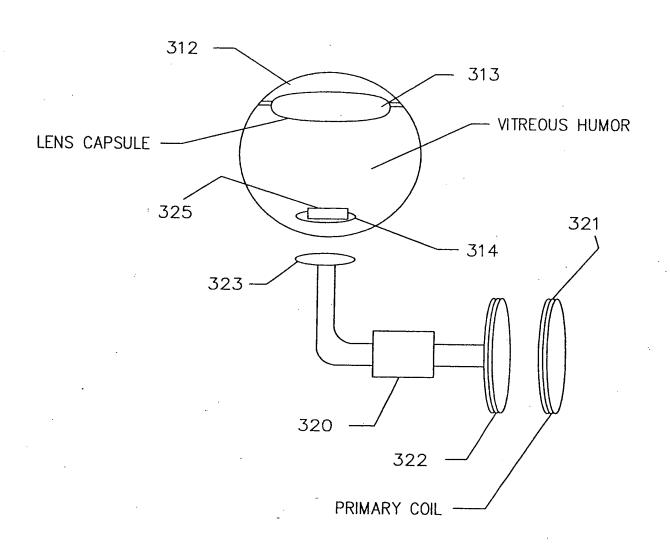
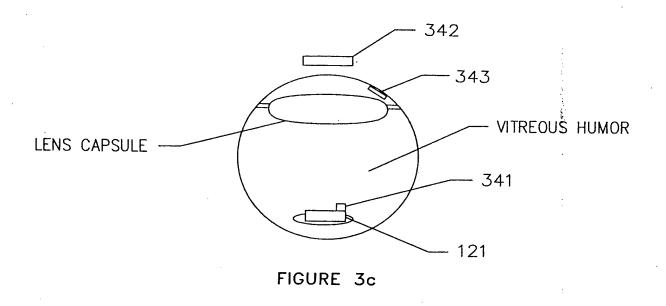


FIGURE 3b



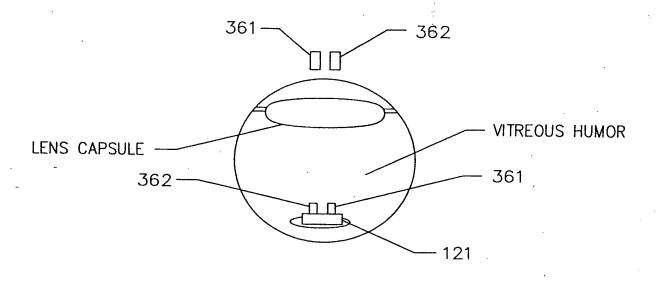
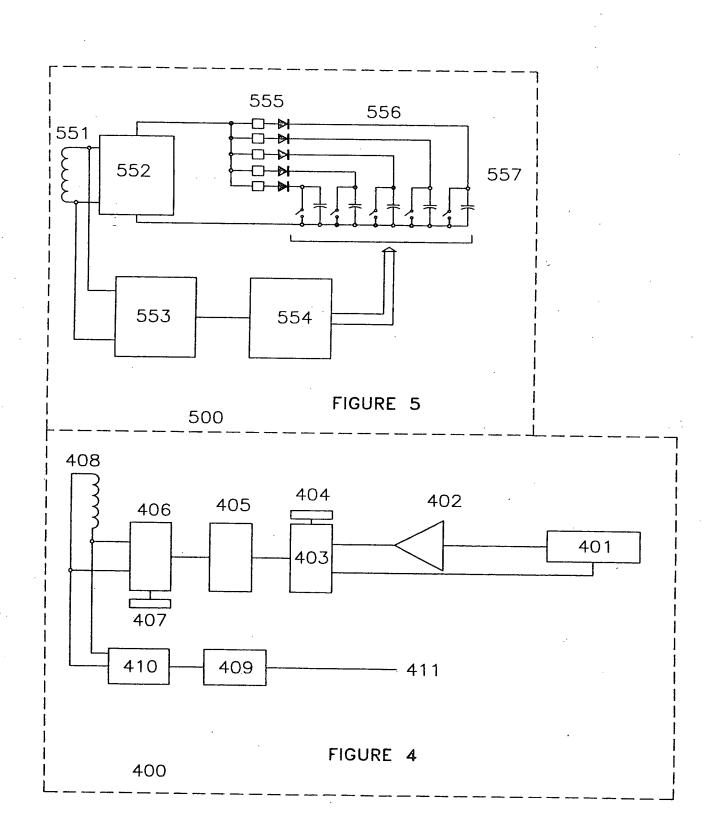
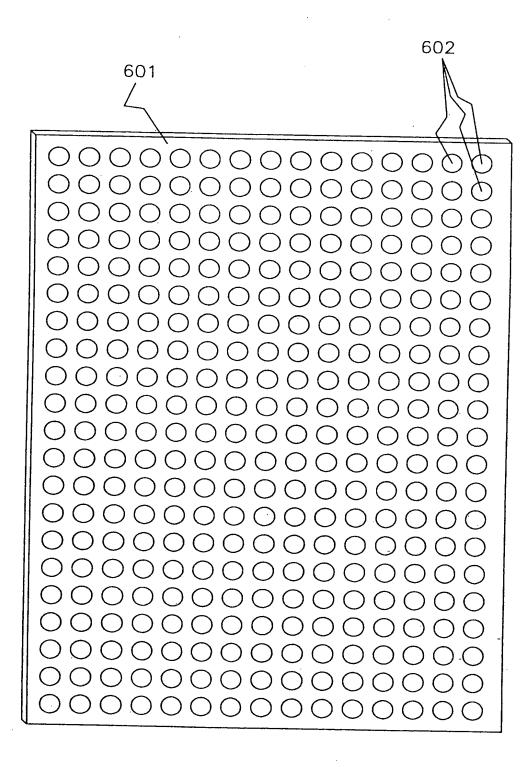
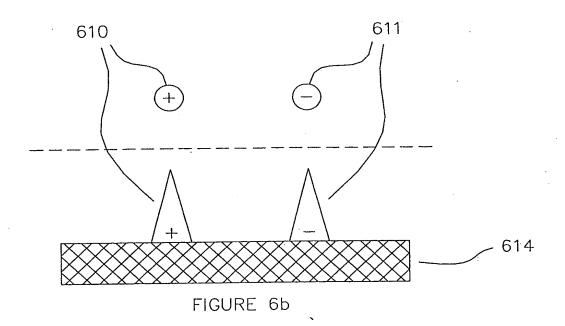


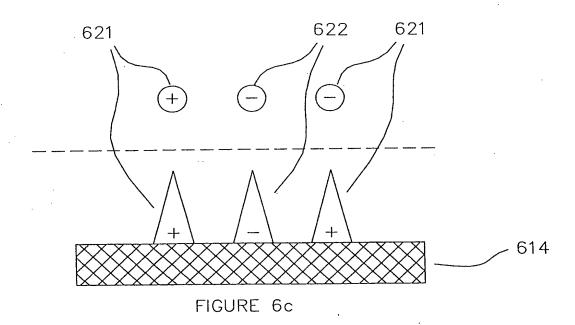
FIGURE 3d

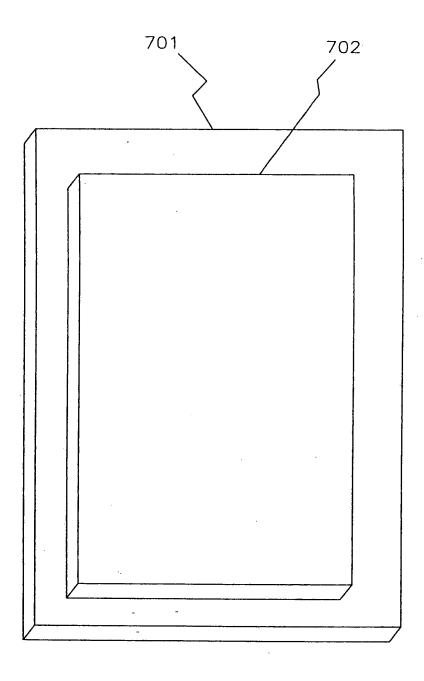




ELECTRODES ON SUBSTRATE FIGURE 6a







INDIFFERENT ELECTRODE OF MONOPOLAR ELECTRODES

FIGURE 7

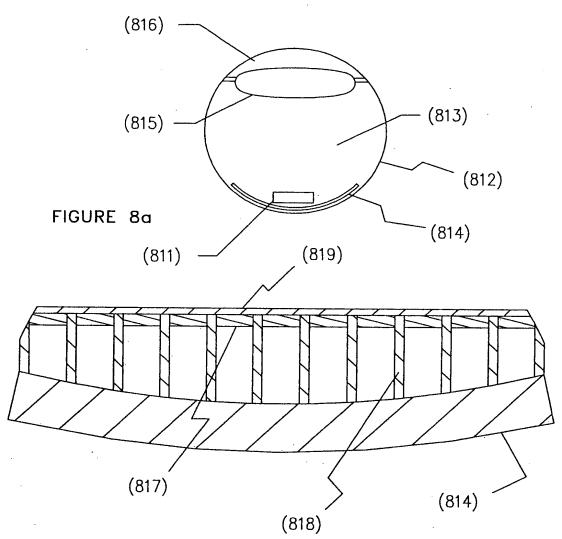


FIGURE 8b EPIRETINAL ELECTRODES — RECESSED

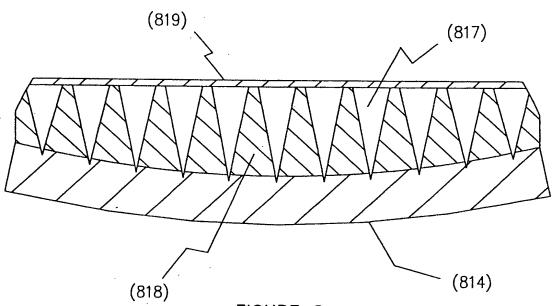


FIGURE 8c EPIRETINAL ELECTRODES - PENERMATING

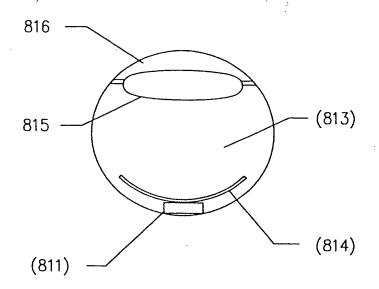


FIGURE 9a

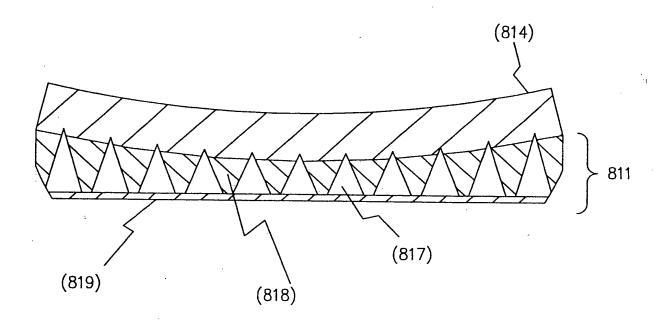
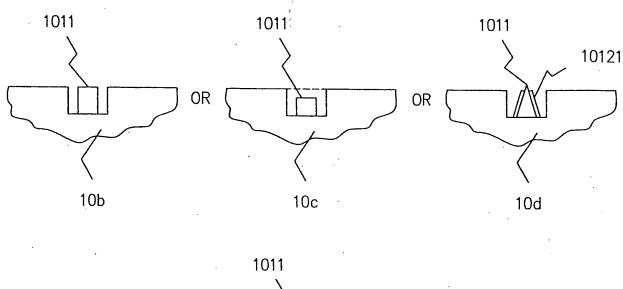
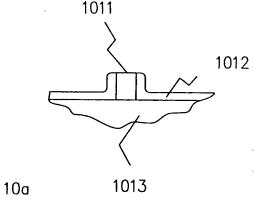
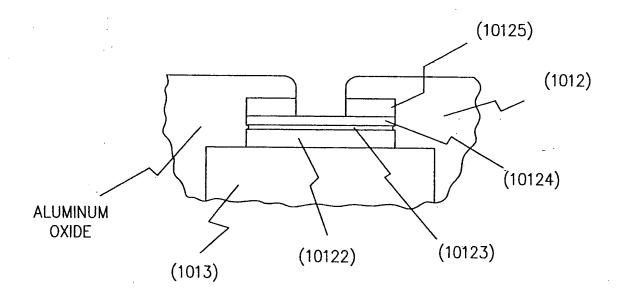


FIGURE 9b SUBRETINAL ELECTRODES — PENETRATING







10e

FIGURE 10

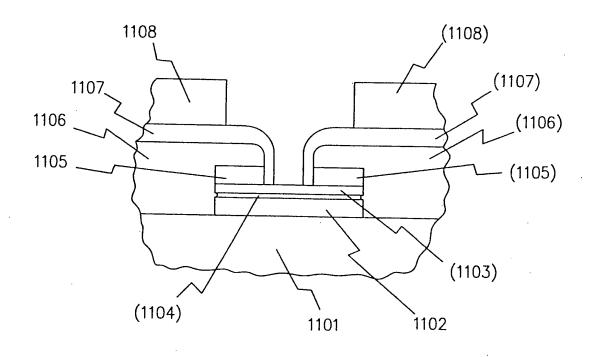


FIGURE 11a ELECTRODES

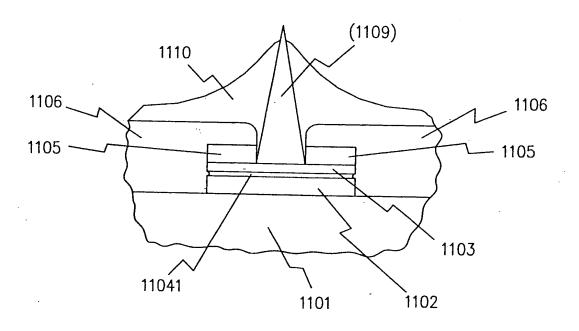


FIGURE 11b ELECTRODES

FIGURE 11

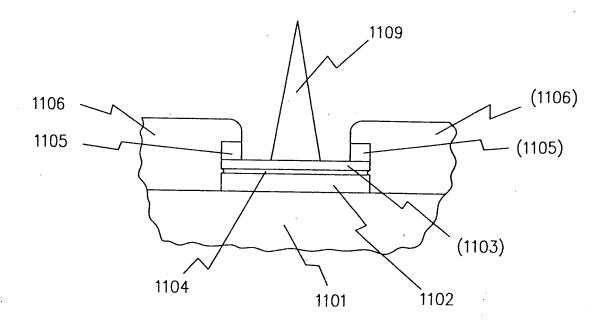


FIGURE 11c

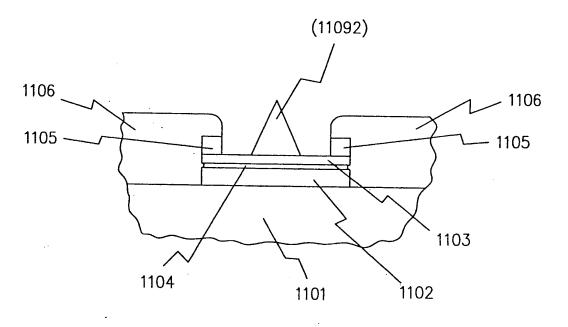


FIGURE 11d

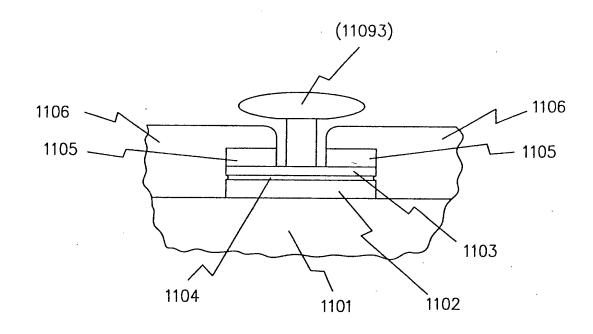
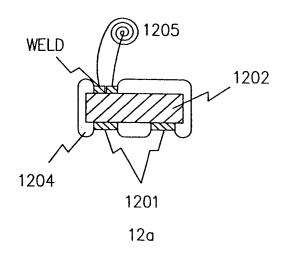
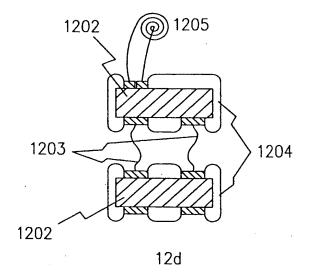
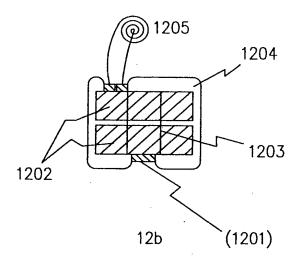


FIGURE 11e







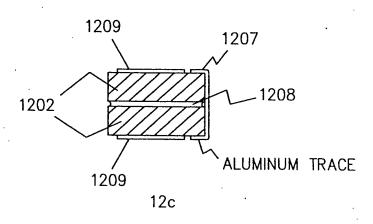


FIGURE 12
COIL AND WIRE ATTACHMENT TO SUBSTRATES AT ELECTRODE

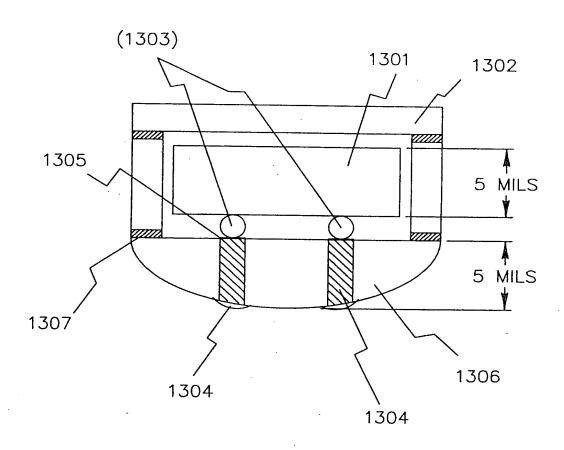


FIGURE 13 HERMETIC SEALING

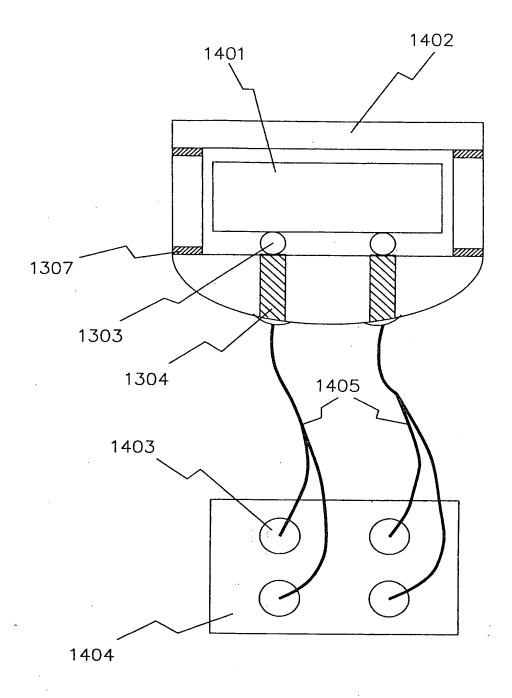


FIGURE 14

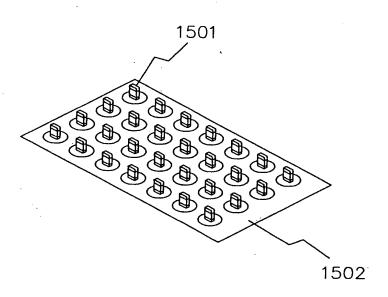
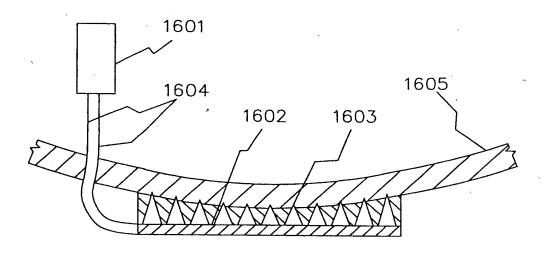
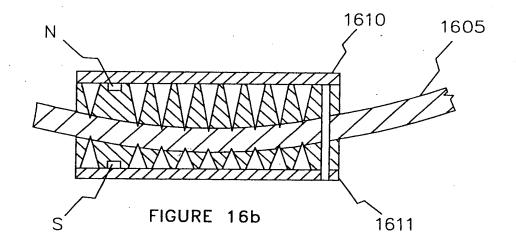


FIGURE 15



FIGURÉ 16a



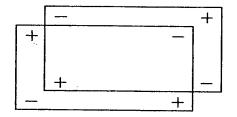


FIGURE 16c

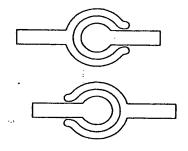


FIGURE 16d

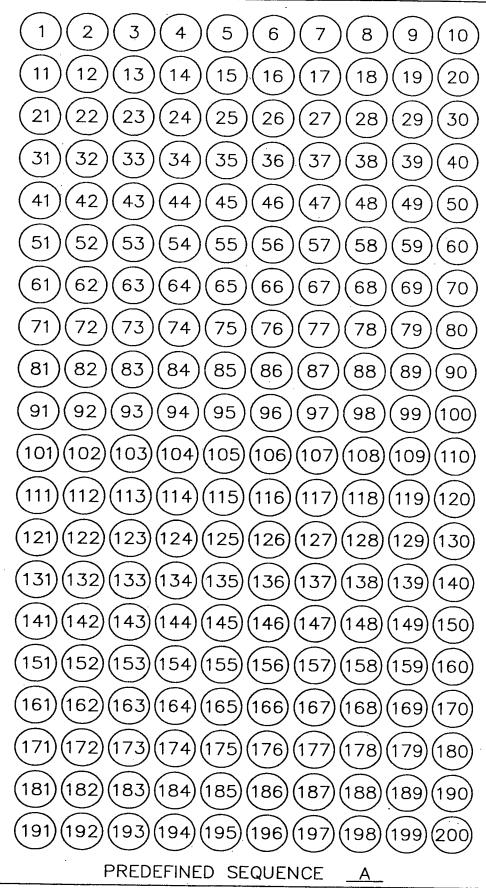
PC PROGRAMMER MAIN SCREEN

RAW VIDEO IMAGE	PROCESSING ALGORITHM SCANNING SEQUENCE GLOBAL ELECTRODE DEFINITION	PROCESSED IMAGE
START		STOP

PROCESSING ALGORITHM

PIXEL	SELE	CTIC	N						
								()	
AVERA	GE _	8		SURF	ROUN	IDINO) PIX	ŒLS	







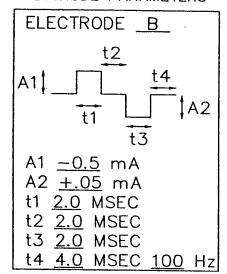


FIGURE 17d

BIPOLAR

	DEFINITION OF PAIRS
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	B 9 + 10 -
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	G 64 ⁺ 65 ⁻
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ı	•
	ETC
L	

FIGURE 17f

GLOBAL ELECTRODE CONFIGURATION

MONOPOLAR BIPOLAR
MULTIPOLAR

FIGURE 17e

MULTIPOLAR

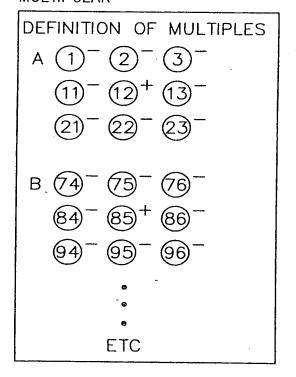


FIGURE 17g

PALM-SIZED TEST UNIT

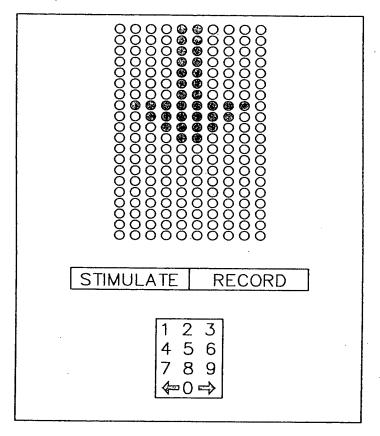


FIGURE 18a

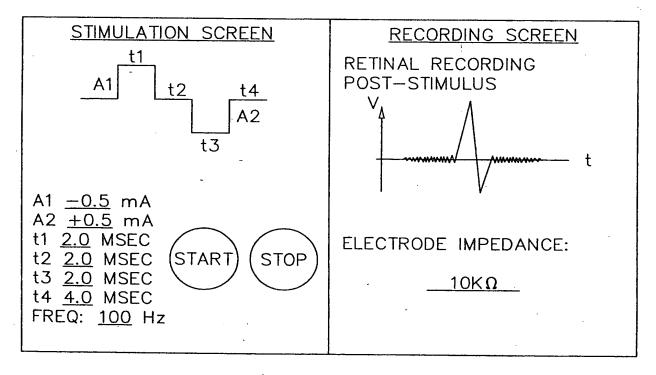


FIGURE 18b

FIGURE 18c

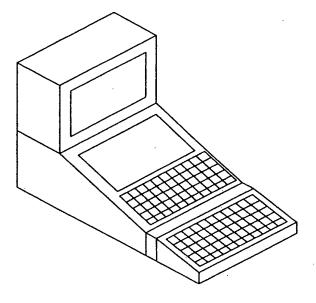


FIGURE 19a

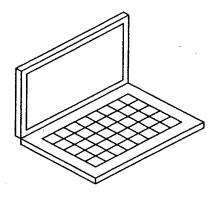


FIGURE 19b

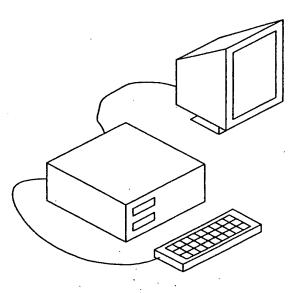


FIGURE 19c

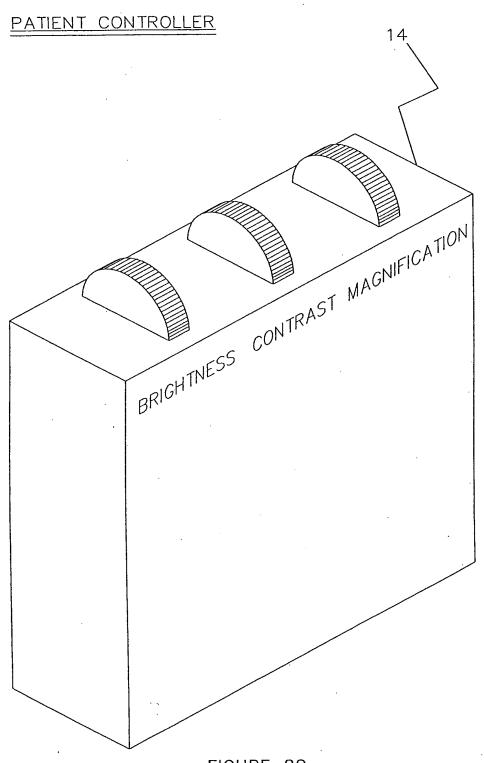


FIGURE 20